

WHAT IS CLAIMED IS:

1 1. A method comprising:

2 generating source code corresponding to a block diagram
3 model; and

4 generating hypertext links associating elements of the
5 generated source code with elements of the block diagram
6 model.

1 2. The method of claim 1 further comprising:

2 displaying the source code and hypertext links on a
3 display;

4 receiving input from a user representing the selection of
5 one of the hypertext links; and

6 displaying to the user at least a portion of the block
7 diagram model including an element of the model associated
8 with the hypertext link.

1 3. The method of claim 2, wherein displaying to the user at
2 least a portion of the block diagram model comprises
3 displaying the associated element in a highlighted fashion.

1 4. The method of claim 1, wherein at least one of the
2 associated elements in the generated source code is a
3 commented reference to a block in the block diagram model.

1 5. The method of claim 1, wherein at least one of the
2 associated elements in the generated source code is a variable
3 reference in an operative code section.

1 6. The method of claim 1 wherein the hypertext link is
2 Standard Generalized Markup Language (SGML).

1 7. The method of claim 1 wherein the hypertext link is
2 Hypertext Markup Language (HTML).

1 8. The method of claim 5 wherein the hypertext language is
2 Extensible Markup Language (XML).

1 9. The method of claim 4 wherein the commented reference to
2 a block comprises a character string identifying a path to a
3 file providing information relating to the sections of the
4 block.

1 10. A system comprising:
2 means for generating source code corresponding to a block
3 diagram model; and
4 means for generating hypertext links associating elements
5 of the generated source code with elements of the block
6 diagram model.

1 11. The method of claim 10 further comprising:

means for displaying the source code and hypertext links
on a display;

means for receiving input from a user representing the
selection of one of the hypertext links; and

means for displaying to the user at least a portion of
the block diagram model including an element of the model
associated with the hypertext link.

12. The method of claim 11, wherein the means for displaying
to the user at least a portion of the block diagram model
comprises displaying the associated element in a highlighted
fashion.

13. The method of claim 10, wherein at least one of the
associated elements in the generated source code is a
commented reference to a block in the block diagram model.

14. The method of claim 10, wherein at least one of the
associated elements in the generated source code is a variable
reference in an operative code section.

15. The method of claim 10 wherein the hypertext link is
Standard Generalized Markup Language (SGML).

16. The method of claim 10 wherein the hypertext link is
Hypertext Markup Language (HTML).

1 17. The method of claim 16 wherein the hypertext language is
2 Extensible Markup Language (XML).

1 18. The method of claim 13 wherein the commented reference to
2 a block comprises a character string identifying a path to a
3 file providing information relating to the sections of the
4 block.

1 19. A computer program product residing on a computer
2 readable medium having instructions stored thereon which, when
3 executed by the processor, cause the processor to:

4 generate source code corresponding to a block diagram
5 model; and

6 generate hypertext links associating elements of the
7 generated source code with elements of the block diagram
8 model.

1 20. The computer program product of claim 19 wherein the
2 computer readable medium is a random access memory (RAM).

1 21. The computer program product of claim 19 wherein the
2 computer readable medium is read only memory (ROM).

1 22. The computer program product of claim 19 wherein the
2 computer readable medium is hard disk drive.

1 23. A processor and a memory configured to:

2 generate source code corresponding to a block diagram
3 model; and
4 generate hypertext links associating elements of the
5 generated source code with elements of the block diagram
6 model.

1 24. The processor and memory of claim 23 wherein the processor
2 and the memory are incorporated into a personal computer.

1 25. The processor and memory of claim 23 wherein the processor
2 and the memory are incorporated into a network server residing
3 in the Internet.

1 26. The processor and memory of claim 23 wherein the
2 processor and the memory are incorporated into a single board
3 computer.